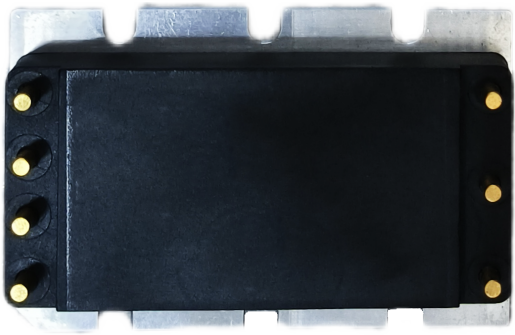


Product Characteristics

- ◆ Input voltage range: 18V-40V
- ◆ Output voltage range: 75%-110%Vout
- ◆ Efficiency≥90%
- ◆ Over-voltage, over-current, short-circuit and over-temperature protection
- ◆ Interior patch design
- ◆ International standard pin mode
- ◆ Three years warranty period



Product Overview

The power module of this model is 18V-40V range input, with a maximum power of 150W and an efficiency of 90%, with input undervoltage and output overvoltage Output overcurrent, output short circuit and over-temperature protection and other protection functions;

Application

The power supply uses an advanced control topology circuit, with advanced power Processing control and packaging technology, with high efficiency, high power density Degree, low noise and other advantages; with input overvoltage protection, output Overcurrent protection, over-temperature protection and other functions;

Absolute rating	Metric	Unit
Voltage between the + IN and the-IN	0.5~50	V
Voltage between the PC and the-IN	0.5~7.0	V
Voltage between PR and-IN	0.5~7.0	V
Voltage between SC and-OUT	0.5~1.5	V
The + OUT and the-OUT voltage	0.5~16.1	V

Product Naming

CFMV	24	C	12	X	150	B	-	
Changfeng MV series standard brick power supply	Input Voltage 24 : 18~40V	A:full brick B:1/2 brick C:1/4 brick	Out Voltage 12 : 12V	M : T _c : -55~100℃ T _s : -65~125℃ H : T _c : -40~100℃ T _s : -55~125℃ T : T _c : -40~100℃ T _s : -40~100℃	Out Voltage 150 : 150W	B:Standard version		Default: non-domestic G: National production

Input Characteristic

parameter	least value	Typical value	crest value	Uuit	working conditions
No-load state input power consumption		1.5	2	W	Tc=25℃
Disable the state input power consumption		0.5	0.8	W	Vpc≤2.3V
Input surge voltage			50	V	Tc=25℃,100ms,the full load
Input underpressure	14.0	14.9	15.9	V	The full load
Input overpressure	40.5	42	43.5	V	The full load

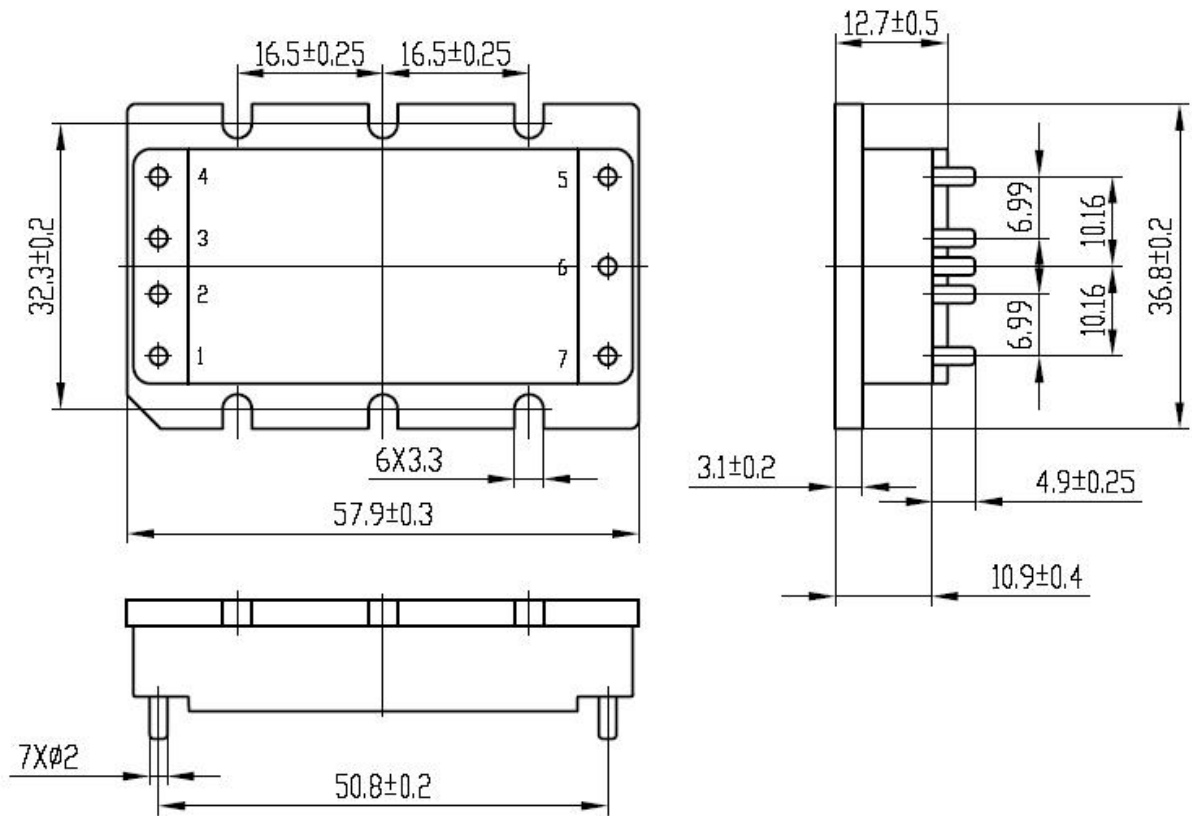
Output Characteristic

parameter	least value	Typical value	crest value	Uuit	working conditions
output current			12.5	A	
Output voltage	11.88	12	12.12	V	Tc=25℃, The full load
Winple and noise voltage		130	200	mV	Bandwidth Bw = 20 MHz,The full load
Voltage regulation		0.05	0.2	%	Vin: 18V-24V, 24V-40V, The full load
Load Regulation		0.05	0.2	%	carrying idler~The full load
temperature coefficient			0.02	%/℃	The full load
Output voltage regulation capability	75		110	%	The full load
Output voltage protection point	13.7		14.9	V	Tc=25℃,carrying idler
Output current limiting protection point	13.5		18.5	A	
short-circuit current			35	A	Tc=25℃, Vo<250mV
Dynamics					
peak deviation		2	5	%	50% -100% -50% load step Rate of change of the output current: 1A / μ S
recovery time		50	100	μs	
Operating voltage of the PC end	5.50		6.00	V	Tc=25℃,Ipc=1.0mA, The full load
The PC terminal working current	2.5		4.5	mA	Tc=25℃,Vpc=5.5V, The full load
The PC terminal has a prohibited voltage	2.3		2.9	V	The full load
The PC terminal alarm voltage	0		0.5	V	Each protection function
PC terminal on output delay		7	15	ms	The full load
The PR output voltage amplitude	1.5		5.0	V	Tc=25℃, The full load
PR drive capability	6			A	Tc=25℃,No buffer amplification circuit
SC reference voltage	1.21	1.23	1.25	V	
SC alarm voltage			0.5	V	Each protection function
efficiency	90			%	Tc=25℃, The full load

Isolation Characteristics

Parameter	Least value	Unit	Working conditions
Insulation and pressure resistance			
Enter to output	3000	V _{AC}	60s, T _c =25℃, The leakage current is less than 3 mA
Input into the shell	1500	V _{AC}	60s, T _c =25℃, The leakage current is less than 1 mA
Output to shell	500	V _{AC}	60s, T _c =25℃, The leakage current is less than 1 mA
Insulation resistance			
Enter to output	200	MΩ	T _c =25℃, 500V _{DC} test
Input into the shell	200	MΩ	T _c =25℃, 500V _{DC} test
Output to shell	200	MΩ	T _c =25℃, 500V _{DC} test

Structural Drawings



Pipe Foot Definition

Pin	Symbol	Function	Pin	Symbol	Function
1	+IN	Enter the positive end	5	-OUT	Output the negative end
2	PC	The original edge control end	6	SC	Secondary control end
3	PR	Parallel end	7	+OUT	Output the positive end
4	-IN	Enter the negative end			